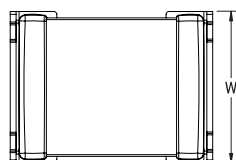
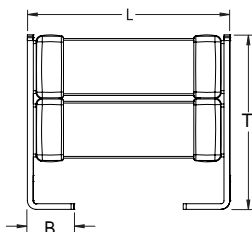


KPS SMD Comm X7R, Ceramic, 4.7 uF, 20%, 100 VDC, X7R, 1210-2

TOP VIEW  
Single or Double Chip Stack



SIDE VIEW  
Double Chip Stack



**Dimensions**

<b>L</b>	3.5mm +/-0.3mm
<b>W</b>	2.6mm +/-0.3mm
<b>T</b>	6.15mm +/-0.15mm
<b>B</b>	0.8mm +/-0.15mm

**Packaging Specifications**

<b>Weight:</b>	135 mg
<b>Packaging:</b>	T&R, 180mm, Plastic Tape
<b>Packaging Quantity:</b>	300

**General Information**

<b>Series:</b>	KPS SMD Comm X7R
<b>Style:</b>	Stacked Chip
<b>Description:</b>	SMD, MLCC, Stacked, Double Chip, Temperature Stable
<b>Features:</b>	Temperature Stable
<b>RoHS:</b>	Yes
<b>Termination:</b>	Tin
<b>AEC-Q200:</b>	No
<b>Chip Size:</b>	1210-2
<b>Shelf Life:</b>	78 Weeks
<b>MSL:</b>	1

**Specifications**

<b>Capacitance:</b>	4.7 uF
<b>Measurement Condition:</b>	1 kHz 1.0Vrms
<b>Capacitance Tolerance:</b>	20%
<b>Voltage DC:</b>	100 VDC
<b>Dielectric Withstanding Voltage:</b>	250 VDC
<b>Temperature Range:</b>	-55/+125°C
<b>Temperature Coefficient:</b>	X7R
<b>Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):</b>	15%, 1kHz 1.0Vrms
<b>Dissipation Factor:</b>	2.5% 1 kHz 1.0Vrms
<b>Aging Rate:</b>	3% Loss/Decade Hour: Referee Time is 48 Hours
<b>Insulation Resistance:</b>	106.4 MOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.